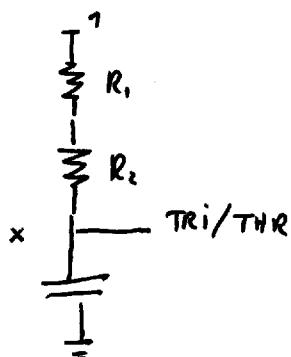


All voltages divided
by V_{CC}
($2/3 \Rightarrow 2/3 V_{CC}$, etc)

Charging cycle (Q open circuit) \bar{Q} is low, OUT = high (1)

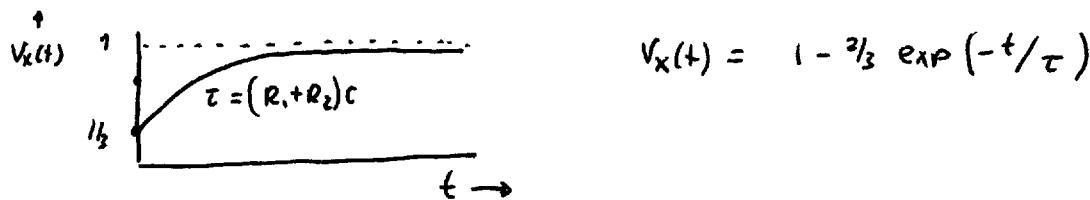


note: no current enters in the opamps
← This is an RC circuit that shows
exponential behavior

$$t = \infty \Rightarrow V_x = 1 \quad (\text{no current through } R_1 \text{ and } R_2)$$

$$t = 0 \Rightarrow V_x = 1/3 \quad (\text{see note later})$$

$$\tau = (R_1 + R_2) C$$

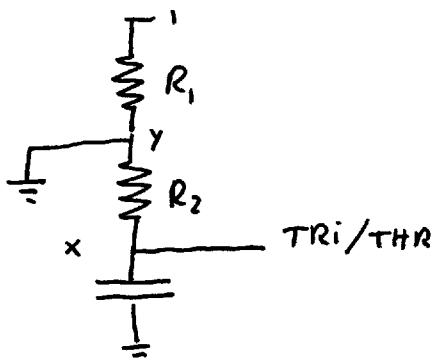


When V_x reaches $2/3$, the top op-amp (OA1) goes from low to high \uparrow . Or, in other words a reset of the flip flop F occurs, OUT \uparrow and $\bar{Q} \uparrow$. Transistor Q opens, shorting point Y to ground.

When does this occur? $V_x(t) = 2/3 \Rightarrow$

$$t_1 = (R_1 + R_2) C \ln 2$$

Now we have

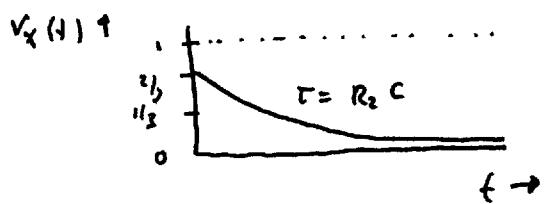


This is an RC circuit that shows exponential behavior

$$t = \infty, V_x = 0$$

$$t = 0, V_x = \frac{2}{3} \text{ (see prev. page)}$$

$$\tau = R_2 C \quad (R_1 \text{ does nothing!})$$

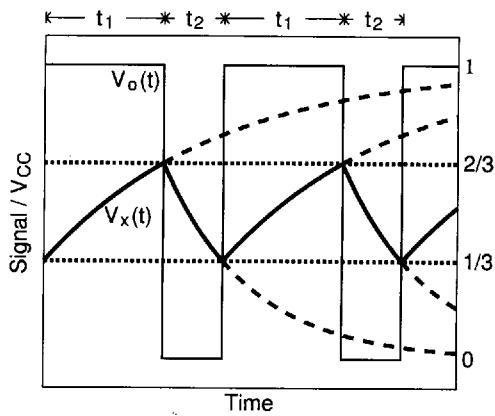


$$V_x(t) = \frac{2}{3} e^{-t/\tau}$$

When V_x reaches $\frac{1}{3}$, the bottom op-amp (OA2) goes from low to high \bar{S} . Or, in other words, a set of the flip-flop occurs, OUT \bar{S} , $\bar{Q} \bar{Z}$. Transistor Q closes (open circuit), removing the ground from point y.

When does this occur? $V_x(t) = \frac{1}{3} \Rightarrow$

$$t_2 = R_2 C \ln 2$$



$$T = t_1 + t_2 = C(R_1 + 2R_2) \ln 2$$

$$f = 1 \text{ kHz} \Rightarrow T = 1 \text{ ms}, C = 100 \text{ nF}, \Rightarrow R_1 + 2R_2 = 10 \text{ k}\Omega$$

$$\text{example } R_2 = 3.9 \text{ k}\Omega, R_1 = 2.2 \text{ k}\Omega$$